

IN THE CLAIMS:

Please amend claims 11, 21, and 23-24 as follows:

1. (Previously Presented) A method for producing a superconducting inductive component having at least two terminals, said method including depositing a stack of alternately superconducting and insulating films comprising at least one line segment incorporating at least one terminal of the component, said line segment constituting a conducting or superconducting layer within.

2. (Previously Presented) The method according to claim 1, wherein depositing of each film constituting the stack is realized so as for said film to be perfectly crystallized.

3. (Previously Presented) The method according to claim 1 further including a prior step of depositing an insulating film on a substrate.

4. (Canceled)

5. (Previously Presented) The method according to claim 1, further including a prior step of depositing a superconducting film on a substrate followed by the depositing of the stack.

6. (Previously Presented) The method according to claim 3, further including the following steps:

a deposit of the stack of alternately superconducting and insulating films,
an etching of the stack carried out in such a way that the stack only remains at the locations where an inductive component is to be implanted.

7. (Previously Presented) The method according to claim 5, further including the following steps:

an etching of the stack carried out in such a way that the stack only remains at the locations where an inductive component is to be implanted.

an etching of the superconducting film.

8-10. (Canceled)

11. (Withdrawn – Currently Amended) A system for producing a superconducting inductive component having at least two ~~plots~~ terminals, said component

comprising at least one line segment incorporating at least one ~~plot~~ terminal of the component, ~~this~~ said line segment constituting a conducting or superconducting layer within a stack of alternately superconducting and insulating films, implementing the method according to claim 1.

12. (Withdrawn) The system according to claim 11, further including:
means for depositing a stack of alternately superconducting and insulating films, and
means for etching all of the deposited films, these said means being arranged in such a way that said deposited films remain only at the locations where an inductive component is to be implanted.

13. (Withdrawn) The system according to claim 11, further including:
means for depositing a superconducting film on a substrate,
means for depositing on the superconducting film a stack of alternately superconducting and insulating films, and
means for etching all of the deposited films, these means being arranged in such a way that the film remains only at the locations where a superconducting line is to be implanted and the stack remains only at the locations where an inductive component is to be implanted.

14-20. (Canceled)

21. (Withdrawn – Currently Amended) A superconducting inductive component having at least two ~~plots~~ terminals, said component comprising at least one line segment incorporating at least one ~~plot~~ terminal of the component, said line segment constituting a conducting or superconducting layer within a stack of alternately superconducting and insulating films.

22. (Withdrawn) The component according to claim 21, wherein each film constituting the stack is perfectly crystallized.

23. (Withdrawn – Currently Amended) The component according to claim 21 wherein at least one of the superconducting films is produced from ~~YBa₂Cu₃O_{7-δ}~~ YBa₂Cu₃O_{7-δ} compounds.

24. (Withdrawn – Currently Amended) The component according to claim 21, wherein at least one of the insulating films is made from ~~LaAlO₃~~ LaAlO₃ compounds.

25. (Withdrawn) An antenna device comprising an electronic circuit including the superconducting inductive component of claim 21.

26. (Withdrawn) The antenna device according to claim 25, wherein the antenna is produced from a superconducting thin film.

27. (Withdrawn) A delay line device comprising an inductive component in series and a capacitive component in parallel downstream of said inductive component, wherein the inductive component is the superconducting inductive component of claim 21.

28. (Withdrawn) A phase shift radar device comprising a plurality of antennas each comprising an electronic circuit including a delay line according to claim 27, said delay line being arranged such that each of said antennas transmits a signal whose phase is shifted with respect to that of the near antennas.

29. (Withdrawn) An electronic frequency filtering device comprising an electronic circuit including the superconducting inductive component of claim 21.

30. (Withdrawn) A high-pass filter device comprising an inductive component in parallel and a capacitive component in series downstream of said inductive component, wherein the inductive component is the superconducting inductive component of claim 21.

31. (Withdrawn) A low-pass filter device comprising a capacitive component in parallel and an inductive component in series downstream of said capacitive component, wherein the inductive component is the superconducting inductive component of claim 21.